## Abstract of the Disclosure

An integrated circuit includes an alignment circuit. The alignment circuit includes a plurality of shift registers that are configured based on training bits transmitted to the integrated circuit via a parallel bus. After the shift registers are configured, the alignment circuit automatically aligns the data bits transmitted to the integrated circuit when the data bits are misaligned by one or more bit time intervals during transmission on the parallel bus.

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"Express Mail" mailing label number: <u>EL873859684US</u>
Date of Deposit: <u>September 26, 2001</u>
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